

# ISPEC 2025

## AGENDA

DATE	TIME	SESSION NAME	BROAD AREA	SESSION THEME	COMPANY / SPEAKER	
<b>Day 0 - Mar 4 - PDC Courses For Industry Engineers at IIT Gandhinagar on Mar 4, 2025</b>						
<b>IIT Gandhinagar, Conference room 1 - Design and devices</b>						
4th March 2025	9:00AM - 10:15AM	Professional Development Courses for Grad Students and Industry Engineers	Design & Devices	Package Design	Prof. Rohit Sharma (IITRPR)	
	10:15AM - 11:30AM			System Design & Architecture	Prof. Binod Kumar (IITJ)	
	11:30AM - 12:45PM			Devices - CMOS	Prof. Nihar Mohapatra (IITGN)	
	12:45PM - 2:00PM	Lunch Break				
	2:00PM - 3:15PM	Professional Development Courses for Grad Students and Industry Engineers	Design & Devices	Devices - Power	Prof. Akshay K (IITBBS)	
	3:15PM - 4:30PM			Integrated Sensors & MEMS	Prof. Bhaskar Mitra (IITD)	
<b>IIT Gandhinagar, Conference room 2 - Package Substrates and Assembly</b>						
	9:00AM - 10:15AM	Professional Development Courses for Grad Students and Industry Engineers	Package Substrates and Assembly	Interconnecting Substrates	Prof. Pradeep Dixit (IITB)	
	10:15AM - 11:30AM			Co-Packaged Optics	Prof. Naresh Emani (IITH)	
	11:30AM - 12:45PM			Materials - Device, Components & Packaging	Prof. Bhagwati Prasad (IISc) Prof. Murali KP (NITC) Prof. Praveen Kumar (IISc)	
	12:45PM - 2:00PM	Lunch Break				
	2:00PM - 3:15PM	Professional Development Courses for Grad Students and Industry Engineers	Package Substrates and Assembly	Predictive Modelling & Design	Prof. Tarun Agarwal (IITGN)	
	3:00PM - 4:30PM			IC & Board Assembly and Reliability	Prof. Nilesh Badwe (IITK)	
<b>IIT Gandhinagar, Conference room 3 - Integrated Packages &amp; Systems</b>						
	9:00AM - 10:15AM	Professional Development Courses for Grad Students and Industry Engineers	Integrated Packages & Systems	Integrated Power Electronics	Prof. Shiladri Chakraborty (IITB)	
	10:15AM - 11:30AM			Thermal Design & Technologies	Prof. Anandaroop B (IITKGP)	
	11:30AM - 12:45PM			6G Integrated Systems	Prof. Mrinal Mandal (IITKGP)	
	12:45PM - 2:00PM	Lunch Break				
	2:00PM - 3:15PM	Professional Development Courses for Grad Students and Industry Engineers		System Level Electrical Test	Prof. Jaynarayan Tudu (IITTP)	
<b>Day 1 - H1 (Main Hall) - INAUGURAL SESSION - ALL TOGETHER- Mahatma Mandir, Gandhinagar</b>						
Date	Time FR	Time TO				
5th Mar 2025	8:15AM	8:23AM		Welcome Remark	IESA/Ashok Chandak	Program start and Welcome Remark By Mr Ashok Chandak, President IESA
	8:23AM	8:35AM	Visionary Keynote	Visionary Keynote	Alpha and Omega (Kaynes)	Visionary Keynote by Dr Madhur Bobde, CTO Alpha Omega Semiconductors
	8:35AM	8:47AM		Keynote Address	Polymatech	Visionary Keynote by Mr Eswara Rao Nandam, Polymatech Electronics Limited
	8:47AM	8:59AM		Keynote address	Siemens EDA	Visionary Keynote By Mr Ruchir Dixit, VP & Country Manager, Siemens EDA, Vice Chairman & BoD, IESA
	8:59AM	9:11AM		keynote address	Tessolve	Visionary Keynote by Mr Srinu Chinamilli, Co-Founder and CEO Tessolve
	9:11AM	9:23AM		Keynote Address	Applied Materials	Visionary Keynote Mr Avinash Avula, Country President & MD-Applied Materials

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<b>Day 1 - H1 (Main Hall) - INAUGURAL SESSION - ALL TOGETHER</b>						
	9:23AM	9:35AM		Keynote Address	NXP	Visionary Keynote by Mr Lars Reger, EVP & CTO, NXP Semiconductors
	9:35AM	9:45AM		Video of GSEM, GIDC and DSIRDA		
	<b>9.45AM</b>	<b>12.20PM</b>	<b>Grand Inaugural Session</b>			
	12:25PM	onwards		Inaugural Address Hon'ble CM, Gujarat		
	12.57PM		Visionary Keynotes	Keynote Address	Resonac	Visionary Keynote by Mr Takatoshi Ikeuchi: Director, Electronics R&D Center Resonac
	<b>1:00PM - 2:00PM</b>	<b>Inaugural</b>	<b>Lunch Break</b>			
<b>Day 1 - H2-Mar 5 - ISPEC Keynote Sessions</b> Session Chairs: Prof. Rao & Navin Bishnoi						
5th March 2025	2:00PM - 2:05PM	ISPEC Keynote Session ISPEC Keynote Session	Welcome address and Platinum Keynote Talks Welcome address and Platinum Keynote Talks	Welcome Remarks	Prof. Rao Tummala	
	2:05PM - 2:30PM			A Vision for Sustainable Manufacturing in India	Prof. Rao Tummala	
	2:30PM - 2:50PM			Semiconductor and Systems for AI & HPC	Gokul V Subramaniam, Intel	
	2:50PM - 3:10PM			Advanced Materials for Wafer Level Packaging and Semiconductor Assembly	Ravi Bhatkal, MacDermid Alpha	
	3:10PM - 3:30PM			Kaynes Advanced Packaging Mfg. & Test in India	Dr. Wasi Uddin, Kaynes Semicon	
	3:30PM - 3:50PM			Advanced Packaging	Gokul Kumar, Micron	
	3:50PM - 4:10PM	ISPEC Keynote Session	Welcome address and Platinum Keynote Talks	Predictive Modeling for Design & Reliability	Prof. Ganesh Subbarayan, Purdue Univ.	
	4:10PM - 4:30PM			A New Collaboration Model to Drive Heterogeneous Integration Roadmap	Loke Yuen Wong, Applied Materials	
	4:30PM - 4:50PM			Trends and Opportunities in IC Power Electronics	Sriram Ramadoss, TI	
	4:50PM - 5:10PM			3D Advanced Packaging	Raja Swaminathan, AMD	
	<b>5:10PM - 5:20PM</b>	<b>Tea Break</b>				
	5:20PM - 5:30PM			Quantum Devices & Applications in India	Prof. Abhisek Dixit, IIT Delhi	
	5:30PM - 5:50PM			Status of semiconductor manufacturing in Gujarat	Ms. Mona Khandhar, Principal Secretary, DST	

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DATE	TIME	SESSION NAME	BROAD AREA	SESSION THEME	COMPANY / SPEAKER	
<b>Day 2 - H2 - Mar 6 - ISPEC R&amp;D Session: Next Gen Research in 12 Strategic Technologies</b> Session Chairs: Ravi M, Intel and Prof. Abhisek D, IITD						
6th March 2025	9:00 - 9:30	ISPEC R&D Session ISPEC R&D Session	Next Gen Research in 12 Strategic Technologies	System Design & Architecture	Prof. Binod Kumar (IITJ)	
	9:30 - 9:45			Package Design	Prof. Rohit Sharma (IITRPR)	
	9:45 - 10:15			Devices - CMOS	Prof. Nihar Mohapatra (IITGN)	
	10:15 - 10:45			Devices - Power	Prof. Akshay K (IITBBS)	
	10:45 - 11:15			Interconnecting Substrates	Prof. Pradeep Dixit (IITB)	
	11:15 - 11:45			Co-Packaged Optics	Prof. Naresh Emani (IITH)	
	11:45 - 12:15			Predictive Modelling & Design	Prof. Tarun Agarwal (IITGN)	
	12:15 - 12:45			6G Integrated Systems	Prof. Mrinal Kanti Mandal (IITKGP)	
12:45 - 14:00						
	14:00 - 14:30	ISPEC R&D Session	Next Gen Research in 12 Strategic Technologies	Integrated Sensors & MEMS	Prof. Bhaskar Mitra (IITD)	
	14:30 - 15:00			ISM - Past & Future	Shri Krishnan S, Secretary, MeitY	
	15:00 - 15:30			Materials - Devices, Components & Packaging	Prof. Bhagwati Prasad (IISc)	
	15:30 - 16:00			IC & Board Assembly and Reliability	Prof. Nilesh Badwe (IITK)	
	16:00 - 16:30			Power Electronics	Prof. Shiladri Chakraborty (IITB)	
	16:30 - 17:00			Thermal Management	Prof. Anandaroop Bhattacharya (IITKGP)	
	17:00 - 17:20		Infrastructure	System Level Electrical Test	Prof. Jaynarayan Tudu (IITTP)	
	17:20 - 17:30			<b>Infrastructure for 12 SRAs</b>	<b>Gaurang Choksi, Ex-Intel</b>	
<b>Day 2 -H2 - Mar 6 - ISPEC Student Poster Session: Next Gen Research Projects in 12 SRAs</b> Session Chairs: Dr. Arun Chandrasekhar, Intel and Prof. Nilesh Badwe, IITK						
6th March 2025	17:30 - 20:00	ISPEC Student Poster Session	Next Gen Research Projects in 12 SRAs	System Design & Architecture	Prof. Binod Kumar (IITJ)	
				Package Design	Prof. Rohit Sharma (IITRPR)	
				Devices - CMOS	Prof. Nihar Mohapatra (IITGN)	
				Devices - Power	Prof. Akshay K (IITBBS)	
	ISPEC Student Poster Session				Interconnecting Substrates	Prof. Pradeep Dixit (IITB)
					Co-Packaged Optics	Prof. Naresh Emani (IITH)
					Predictive Modelling & Design	Prof. Tarun Agarwal (IITGN)
					6G Integrated Systems	Prof. Mrinal Kanti Mandal (IITKGP)
					Integrated Sensors & MEMS	Prof. Bhaskar Mitra (IITD)
					Materials - Device & Packaging	Prof. Bhagwati Prasad (IISc)
					IC & Board Assembly and Reliability	Prof. Nilesh Badwe (IITK)
					Power Electronics	Prof. Shiladri Chakraborty (IITB)
	Thermal Management	Prof. Anandaroop Bhattacharya (IITKGP)				
					System Level Electrical Test	Prof. Jaynarayan Tudu (IITTP)
	<b>Day 3 - Mar 7 - ISPEC Workforce Session</b> Session Chairs: Ravi Bhatkal, MacDermid & Prof. Shiv Govind, IITH					
	7th March 2025	09:00AM - 09:20AM	ISPEC Workforce Session	Workforce Session	Workforce Needs in India	Manish Hooda, MeitY
09:20AM - 09:40AM		Manufacturing Skill Development			Dr. Ashwini Aggarwal	
09:40AM - 10:00AM		Current Technology Workforce			Sharat Kaul, Kaynes	
10:00AM - 10:20AM		Next-gen Technology Workforce			Prof. Nilesh Badwe, IITK	
<b>Day 3 - Mar 7 - ISPEC Poster Awards Session</b> Session Chairs: Prof. Nilesh B, IITK and Arun Chandrasekhar, Intel						
7th March 2025	10:20AM-11:30AM	ISPEC Student Poster Awards Session	Student Poster Awards Session	Poster Awards Session	Prof. Nilesh Badwe, IITK & Arun Chandrasekhar, Intel	



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DATE	TIME	SESSION NAME	BROAD AREA	SESSION THEME	COMPANY / SPEAKER
<b>Day 3 – Mar 7 - ISPEC Supply Chain Session</b> Session Chairs: Venky S, 3D System Scaling & Kuldip J, MKS Atotech					
7th March 2025	11:30 - 11:45	ISPEC Supply Chain Session	Session Introduction	Global & Indian Packaging Supply Chain Ecosystem	Venky Sundaram, 3D System Scaling and Kuldip Johal, MKS Atotech
	11:45 - 12:00		Global & Indian Packaging Keynotes	Power Semiconductor Ecosystem View	CDIL
	12:00 - 12:15			Kaynes	Hernanie Torres, Kaynes Semicon
	12:15 - 12:30		India OSAT Keynote Session	Tata TSAT	TBA
	12:30 - 12:45			CG Semi	J Y Kim, CG Semi
	12:45 - 13:00			Indian Semiconductor, OSAT/Packaging Ecosystem Driving Future Supply Chain Demand	Moderators: Venky Sundaram, Kuldip Johal & All Speakers from above Sessions
	13:00 - 13:30		Panel Discussion #1		
13:30 - 14:30	<b>Lunch Break</b>				
7th March 2025	14:30 - 14:45	ISPEC Supply Chain Session	Materials and Chemicals Keynote	Materials - EMC, Laminates	Sumitomo Bakelite
	14:45 - 15:00			Materials/Chemicals - Metallization	Naveen Goudar, MKS Atotech Instruments
	15:00 - 15:15			Materials/Chemicals - Metallization	Taiyo Ink
	15:15 - 15:30			Materials - Polymers	Ram Trichur, Henkel
	15:30 - 15:45			Materials - Glass for electronic packaging	Dr. Akash Bhatnagar, Schott AG
	15:45 - 16:00		Substrates, Assembly & Test, Equipment Supply Chain	Panel/Wafer Tools - Swiss PVD Solutions for Advanced Packaging and beyond	Maurus Tschirky, Evatec AG
	16:00 - 16:15			Panel Tools - Litho, Metrology	Nathan Peng, Onto Innovation
	16:15 - 16:30			Ceramic Substrates - Emerging Trend of Ceramic Packaging and Technology	Atsushi (Adam) Kuhara, Niterra Group (Takuya Torii)
	16:30 - 16:45			Modeling Tools	Anil Kumar, Ansys
	16:45 - 17:15		Panel Discussion #2	What Motivation is Needed to Establish Leading Edge Supply Chain Capabilities in India?	Moderator: Venkata Mokkupati, AT&S & All Speakers from Above Sessions
<b>Day 4 – Mar 8 - Site Visit to Dholera &amp; Sanand</b>					
8th March 2025	9:00AM - 5:00PM	Site Visit to Dholera and Sanand	Visit to industrial Site 9 AM - 5 PM. On pre approval basis. Two separate routes. Registration link will be updated shortly in webpage.	Gujarat at the Heart of the Semiconductor Manufacturing Revolution: Exploring Dholera and Sanand Sites	Dholera/ Sanand visit
<b>EXHIBITION</b>					
5th Mar 2025	12:45PM	Exhibition	Inauguration of Exhibition by 12.45 PM	Sand to Silicon to Systems : Experience the End-to-End Ecosystem Journey	
	1:00PM - 6:00PM		Exhibition Open for delegates		
6th Mar 2025	9:15AM - 1:00PM		Exhibition Open for delegates		
	1:00PM - 6:00PM		Exhibition Open for delegates		
7th Mar 2025	9:15AM - 1:00PM		Exhibition Open for delegates		
	1:00PM - 6:00PM		Exhibition Open for Students and Public		



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Date	TIME	Session Name	Broad Area	Session Theme
<b>PARALLEL SESSIONS</b>				
5th Mar 2025			India Taiwan Roundtable	45-50 CXO Roundtable
			Roundtable with Infineon	30-35 Stakeholders roundtable
			Meetings in Meeting Rooms 1-9	Key Sponsor/Stakeholder/Govt Meetings
6th Mar 2025			India Japan Roundtable	30-40 Stakeholders Roundtable
			Meetings in Meeting Rooms 1-9	Key Sponsor/Stakeholder/Govt Meetings
			Round table with Embassies	Embassies
			India Singapore - round table	20-25 Stakeholders
5th/6th/7th Jan 25			Anchor Investors - meeting rooms	
			GoG - meeting room	
			Gol - meeting room	
			Korea - Round table - To be decided	
			US Country Round table - To be decided	